

ABSTRACT OF THE DISCLOSURE

An AD converter includes a sample-&-hold circuit which samples and holds an input analog potential in a first period, and generates a signal
5 indicative of a magnitude relation between the held input analog potential and a reference potential in a second period, a plurality of amplifiers connected in series which amplify an output of the sample-&-hold circuit, and a control circuit which controls
10 operating timing of the amplifiers so as to make at least one of the amplifiers start operating in a middle of the first period.